

Electronic Acknowledgement Receipt

EFS ID:	1349709
Application Number:	10779781
International Application Number:	
Confirmation Number:	1632
Title of Invention:	Flat panel display device with polycrystalline silicon thin film transistor
First Named Inventor/Applicant Name:	Ji-Yong Park
Customer Number:	58027
Filer:	Hae-Chan Park/Richard Schachner
Filer Authorized By:	Hae-Chan Park
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Application Type:	Utility

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1		2006-12-4-61610111US-Reply-filed-with-USPTO.pdf	174454	yes	21

Multipart Description/PDF files in .zip description			
	Document Description	Start	End
	Amendment After Final	1	1
	Claims	2	17
	Applicant Arguments/Remarks Made in an Amendment	18	21

Warnings:	
Information:	
Total Files Size (in bytes):	174454

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371
If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.